To:

Facsimile Number: 571-273-8300

From:

Texas Instruments Incorporated Facsimile: 972-917-4418

Total Pages Sent 3
RECEIVED
CENTRAL FAX CENTER

MAR 2 7 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant:

Vijay K. Reddy, et al.

Docket Number: TI-37048

Serial No.: 10/814,453

Art Unit: 2858

Filed: 3/31/04

Examiner: Jeff W. Natalini

For: Voltage Waveform Generation Circuit

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below:

Karen Vertz

3-27-06

FACSIMILE COVER SHEET

X FACSIMILE COVER SHEET NEW APPLICATION DECLARATION ASSIGNMENT FORMAL DRAWINGS INFORMAL DRAWINGS CONTINUATION APP'N DIVISIONAL APP'N		AMENDMENT (# pages) X EOT 1 month X NOTICE OF APPEAL APPEAL (# pages) ISSUE FEE & PUBLICATION FEE (1 Page) REPLY BRIEF (# Pages)
NAME OF INVENTOR(S):		RECEIPT DATE & SERIAL NO.:
Vijay K. Reddy, et al. TITLE OF INVENTION: Voltage Waveform Generation Circuit		Serial No.: 10/814,453 Filing Date: 3/31/04
TI FILE NO.:	DEPOSIT ACCT, NO.:	
TI-37048	20-0668	
FAXED: 3-27-06 DUE: 3-6-06		-
ATTY/SECY: AKS/kjv		

This facsimile is intended only for the use of the address named and contains legally privileged and/or confidential information. If you are not the intended recipient of this telecopy, you are hereby notified that any dissemination, distribution, copying or use of this communication is strictly prohibited. Applicable privileges are not walved by virtue of the document having been transmitted by Facsimile. Any misdirected facsimiles should be returned to the sender by mail at the address indicated on this cover sheet.

Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265-5474